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Applicant(s): **Bartenstein et al.**

Docket No.

BUR920000217US1

Application No.

09/927,011

Filing Date

8/9/2001

Examiner

S. Stephen Dildine Jr.

Group Art Unit

2133

Invention: **INTERNAL CACHE FOR ON CHIP TEST DATA STORAGE**

I hereby certify that this Amendment (13 pages)

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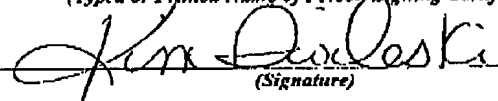
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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of: Bartenstein et al.

Docket No.: BUR920000217US1

Serial No.: 09/927,011

Group Art Unit: 2133

Filed: August 9, 2001

Examiner: R. Stephen Dildine Jr.

Title: INTERNAL CACHE FOR ON CHIP TEST DATA STORAGE

Commissioner for Patents & Trademarks
P. O. Box 1450
Alexandria, VA 22313-1450

AMENDMENT

This paper is being filed in response to the Office Action mailed on August 13, 2004.

Applicants request this Amendment be entered in the above-identified application and reconsideration of the application in view of the Amendments and Remarks that follow.